

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Group Art Unit: To Be Assigned

Examined To Be Assigned

**10/20 REC'D PTO 27 APR 2006**

In Re PATENT APPLICATION OF:

Applicant : Takamitsu YAMANAKA )

U.S. Appln. No. : To Be Assigned )

International Appln. No.: PCT/ JP05/014208 )

International Filing Date: August 3, 2005 )

Priority Date Claimed : August 17, 2004 )

For : SEMICONDUCTOR DEVICE AND ITS )  
MANUFACTURING METHOD )

Attorney Ref. : AI 409NP )

**INFORMATION  
DISCLOSURE  
STATEMENT**

April 27, 2006

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

This is an Information Disclosure Statement submitted in compliance with the timing requirements of 37 C.F.R. § 1.97(b)(2).

Attached are copies of twelve Japanese patents, any relevance of which to the claimed invention can be gleaned from the attached English-language Abstracts, the attached English-language international search report, computer translations, the U.S. counterpart patents, and/or from page 3 of the specification, where some of the documents are discussed. All of the publications are listed on the attached Form PTO-1449. Also listed on the Form PTO-1449 are two U.S. Patents and two U.S. Patent Application Publications for consideration.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§ 1.97 and 1.98 are deemed to be fully met as to all documents listed. Consideration of the listed documents is respectfully requested.

Respectfully submitted,

Robert H. Berdo, Jr. (Reg. No. 38,075)  
RABIN & BERDO, P.C.  
Customer No. : 23,995  
Telephone: 202-371-8976  
Telefax: 202-408-0924

April 27, 2006  
Date

RHB:tl

**FEE ENCLOSED: \$ 40.00**  
Please charge any further  
fee to our Deposit Account  
No. 18-0702

IAP20 Rec'd PCT/PTO 27 APR 2006

<b>FORM PTO-1449</b> <b>INFORMATION DISCLOSURE STATEMENT</b>				<b>Docket No.:</b> <b>AI 409NP</b>		<b>International No.:</b> <b>PCT/JP05/074208</b> <b>10/577457</b>	
<b>Applicant:</b> <b>Takamitsu YAMANAKA</b>							
<b>International Filing Date:</b> <b>August 3, 2005</b>				<b>Group Art Unit:</b> <b>To Be Assigned</b>			
<b>U.S. PATENT DOCUMENTS</b>							
<b>Examiner Initial</b>	<b>Document Number</b>	<b>Date</b>	<b>Name</b>	<b>Class</b>	<b>Sub-Class</b>	<b>Filing Date</b>	
	2003/0022460	01/30/03	Park				
	5,236,861	08/17/93	Otsu				
	5,760,440	06/02/98	Kitamura et al.				
	2002/0025632	02/28/02	Hayashi et al.				
<b>FOREIGN PATENT DOCUMENTS</b>							
	<b>Document Number</b>	<b>Date</b>	<b>Country</b>	<b>Class</b>	<b>Sub-Class</b>	<b>Trans-lation</b>	
	06-268162	09/22/94	JAPAN			Abstract and computer translation	
	05-082637	04/02/93	JAPAN			Abstract and computer translation	
	2003-60025	02/28/03	JAPAN			Correspond to USPAP 2003/0022460	
	60-137024	07/20/85	JAPAN			Abstract	
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	10-284615	10/23/98	JAPAN			Abstract and computer translation	
	2002-76288	03/15/02	JAPAN			Correspond to USPAP 2002/0025632	
<b>OTHER (Including Author, Title, Date, Pertinent Pages, etc.)</b>							
<b>Examiner</b>	/Swapneel Chhaya/				<b>Date Considered</b> 12/19/2008		
EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.							